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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ebenezer E. Eshun, et al.

Examiner: Unassigned

Serial No.: 10/707,863

Art Unit: 2811

Filed: January 19, 2004

Docket: BUR920030058US1 (16693)

For: HIGH TOLERANCE TCR BALANCED
HIGH CURRENT RESISTOR FOR RF CMOS
AND RF SiGe BiCMOS APPLICATIONS AND
CADENCED BASED HIERARCHICAL
PARAMETERIZED CELL DESIGN KIT WITH
TUNABLE TCR AND ESD RESISTOR
BALLASTING FEATURE

Dated: May 24, 2004

Confirmation No: 1862

Mail Stop: Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

In accordance with 37 C.F.R. §§ 1.115 and 1.121, it is requested that the following amendments be entered in the present application.

Amendments to the Specification begin on page 2 of this paper.

Remarks begin on page 3 of this paper.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Missing Parts, Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450 on September 11, 2003

Dated: May 24, 2004


Steven Fischman